Question 1 T/F An LRU replacement policy will always be better than a random replacement policy for managing virtual memory pages.

Question 2 T/F One of the main benefits of scoreboarding the CPU is the reduction of WAR and WAW hazards through register renaming.

Question 3 T/F With single-issue, in-order execution, and the classical five-stage pipeline with no bypassing, WAW hazards never cause any stalls in the pipeline.

Question 4 T/F Cache memory is the fastest part of register memory.

Question 5 T/F An assembler takes binary code and translates it into assembly language for the processor to execute.